

	(d)	In IC-555 pin no. 3 is terminal.	1
		(i) Output	
		(ii) Reset	
		(iii) Threshold	
		(iv) Discharge	
	(B) Ansv	wer any two of the following :	
	(a)	Explain the working of Half Wave Rectifier with capacitor input filter using its circuit diagram and waveform.	3
	(b)	Explain with circuit diagram working of operational amplifier as buffer.	3
	\©	Draw internal block diagram of IC 555 and explain the function of each block.	3
2.	(A) Ans	wer any two of the following:	3
	(a)	How CRO is used for frequency and phase measurement? Explain it with suitable diagram.	3
	(b)	Explain the following parameters in case of voltage regulators :	3
		(i) Line Regulation	
		(ii) Load Regulation	
		(iii) Ripple Rejection	
	16)	Draw voltage regulator circuit using IC LM317 and explain it's working. Also state equation for its output voltage.	3
	(B) Atte	mpt any one of the following :	
	(a)	Explain the working of operational Amplifier schmitt trigger with the help of circuit diagram and transfer characteristics.	4
	(b)	Compare AM with FM. (Any four points)	4
3.	(A) Anso	wer any two of the following:	
	(a)	Explain how CRO displays the waveform.	3
	(If centre tap full wave rectifier is connected across secondary of 12-0-12 volts of centre tap transformer. Whose primary is connected to AC mains supply of 230V, 50Hz then find out:	3
		(i) Average Output Voltage	
		(ii) PIV Rating of Diode	
	(6)	Explain the working of Non Inverting Amplifier using operational amplifier. Derive the formula for its output voltage.	3
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	(B) Ansv	ver any one of the following :	
	(a)	What is Displacement Transducer ? Explain the working of linear variable differential transformer with its diagram.	.4
	(b)	 In amplitude modulation a sinusoidal carrier wave of frequency 2MH, and amplitude 100V is modulated. 	
		(ii) Sinusoidal wave of frequency 10KHz producing 50% modulatoin, calculate (1) Amplitude of sidebands (2) frequencies of USB and LSB (3) Bandwidth.	4
4.	(A) Ansv	wer any two of the following:	
	(a)	Explain any three front pannel controls of CRO.	3
	(b)	Explain the block diagram basic Idea of SMPS.	3
	(c)	In an Inverting adder circuit using operational amplifier the input voltages are $V_1 = 400 \text{mV}$, $V_2 = 200 \text{mV}$ and $V_3 = 100 \text{mV}$, if input resistance $R_1 = 4K\Omega$, $R_2 = 2K\Omega$ and $R_3 = 1K\Omega$. Find the output voltage	
		for Rf = $10K\Omega$.	3
	(B) Ans	ewer any one of the following:	
	(a)	Explain the working of Inverting Amplifier using Op-Amp with neat diagram. How it can be used as a sign changer?	4
	(b)	What is Satellite? Explain use of satellite as a Relay Station.	4
5.	(A) Ans	swer any two of the following:	
	(a)	Explain the function of Delay Line and blanking circuit in CRO.	3
	(b)	In a Zener diode voltage Regulator circuit with series resistor of 200Ω . Find the Zener Current, Source Current and Load Current. If input voltage Vs is 20V, Breakdown voltage Vz is 15V and a load resistance is $1K\Omega$.	3
	(c)	Draw basic block diagram of Communication System and explain it in brief.	3
	(B) An	swer any one of the following:	
	(a)	Explain the working of Thermistor and LDR with its constructional diagram.	4
	(P)	State any four characteristics of an Ideal Operational Amplifier. OR	4

5.	(A)	Ans	wer any two of the following:	
		(5)	Draw the functional diagram of Electrostatic Focussing System and explain it in detail.	,
		(b)	Define the following terms in case of operational amplifier:	3
			(i) Input Offset Current	
			(ii) Input Offset Voltage	
			(iii) Output Offset Voltage	
		(c)	Explain the working of Astable Multivibrator using IC 555 with its circuit diagram.	,
	(B)	Ansv	wer any one of the following :	٠
		(a)	Draw the block diagram of DC regulated power supply and explain the working of each block.	4
		(b)	What is Network Topology ? Explain Star, Ring and Bus Topology with suitable diagram.	4